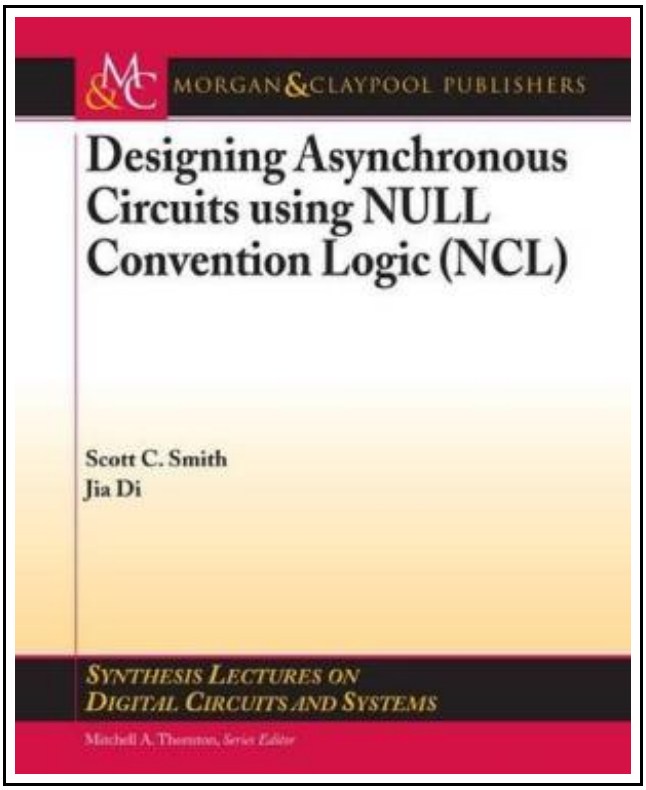
Designing Asynchronous Circuits Using Null Convention Logic (Ncl)



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Reviews

Simply no phrases to clarify. It is really basic but surprises from the 50 percent of the ebook. Once you begin to read the book, it is extremely difficult to leave it before concluding.

(Mr. Noah Cummerata IV)

DESIGNING ASYNCHRONOUS CIRCUITS USING NULL CONVENTION LOGIC (NCL)

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Morgan & Claypool. Paperback. Book Condition: New. Paperback. 96 pages. Dimensions: 9.1in. x 7.3in. x 0.4in.Designing Asynchronous Circuits using NULL Convention Logic (NCL) begins with an introduction to asynchronous (clockless) logic in general, and then focuses on delay-insensitive asynchronous logic design using the NCL paradigm. The book details design of input-complete and observable dual-rail and quad-rail combinational circuits, and then discusses implementation of sequential circuits, which require datapath feedback. Next, throughput optimization techniques are presented, including pipelining, embedding registration, early completion, and NULL cycle reduction. Subsequently, low-power design techniques, such as wavefront steering and Multi-Threshold CMOS (MTCMOS) for NCL, are discussed. The book culminates with a comprehensive design example of an optimized Greatest Common Divisor circuit. Readers should have prior knowledge of basic logic design concepts, such as Boolean algebra and Karnaugh maps. After studying this book, readers should have a good understanding of the differences between asynchronous and synchronous circuits, and should be able to design arbitrary NCL circuits, optimized for area, throughput, and power. Table of Contents: Introduction to Asynchronous Logic Overview of NULL Convention Logic (NCL) Combinational NCL Circuit Design Sequential NCL Circuit Design NCL Throughput Optimization Low-Power NCL Design Comprehensive NCL Design Example This item ships from multiple locations. Your book may arrive from Roseburg,OR, La Vergne,TN. Paperback.

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